

# Anxiao (Andrew) Jiang

## Contact Information

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## Mailing Address

Department of Computer Science and Engineering  
Texas A&M University  
College Station, TX 77843-3112

## Research Areas

Information theory, data storage, algorithm design.

## Education

- Sep 2000 – June 2004 Ph.D. in Electrical Engineering (Parallel and Distributed Systems Lab)  
**California Institute of Technology**, Pasadena, CA  
Overall GPA: 4.2/4.0
- Sep 1999 – June 2000 M.S. in Electrical Engineering (Parallel and Distributed Systems Lab)  
**California Institute of Technology**, Pasadena, CA  
Overall GPA: 4.1/4.0
- Sep 1994 – July 1999 B.S. in Electronic Engineering, **Tsinghua University**, Beijing, China  
Major in Electronic and Information Systems, 5-year program  
Bachelor degree with honors.

## Experience

- Sep 2011 – present Associate Professor, Computer Science and Engineering Department,  
Texas A&M University
- Aug 2005 – Aug 2011 Assistant Professor, Computer Science and Engineering Department,  
Texas A&M University
- July 2011 Visiting professor at EPFL, Lausanne, Switzerland
- June 2011 – July 2011 Consulting Researcher, HP Labs
- Aug 2009 – Apr 2010 Consulting Researcher, EMC Corporation
- June 2009 Consulting Researcher, Microsoft Research in Redmond
- July 2008 – Aug 2008 Visiting professor at the CMI (Center for the Mathematics of Information),  
California Institute of Technology
- July 2004 – July 2005 Postdoctoral Fellow, Electrical Engineering Department,  
California Institute of Technology

## Teaching Experience

- Assistant Professor Department of Computer Science and Engineering, **Texas A&M University**  
Analysis of Algorithms, Database Systems, Ad hoc and Sensor Networks,  
Wireless Sensor Networks, Information Processing in Sensor Networks,  
Frontiers in Storage Systems
- Teaching Assistant Departments of Computer Science and Electrical Engineering, **Caltech**  
Wireless Communications, Networking, Error Correcting Codes,  
Information Theory

**Honors and Awards**

2012 - present	IEEE Senior Member
2010	The 2009 IEEE Communications Society Best Paper Award in <i>Signal Processing and Coding for Data Storage</i>
2008	NSF (National Science Foundation) CAREER Award
1999-2003	Caltech Engineering Division Four-Year Fellowship
1999	Caltech Shannon Prize
1999	B.S. degree with Honors, Tsinghua University
1996	Honor of Distinguished Undergraduate Student of Tsinghua University
1996	Special Research Training Program for Outstanding Students
1995	Second Rank Award in University Physics Contest, Beijing, China
1994-1999	Undergraduate fellowships at Tsinghua University every year

**Research Funding**

US-Israel Binational Science Foundation Award, 2011-2015

NSF CAREER Award, 2008-2013

NSF IHCS (Integrative, Hybrid and Complex Systems) Fund, 2008-2011

**Patents**

US patent No. 7,752,332, "Geometric Routing in Wireless Networks," A. Jiang, J. Gao and J. Bruck.

US patent No. 7,656,706, "Storing Information in a Memory," A. Jiang, V. Bohossian and J. Bruck.

Six additional non-provisional and provisional patents.

**Selected Talks and Research Impact**

Coding for Flash Memories, Seminar at IBM Almaden Research Center, July 2011.

Coding for Flash Memories, HP Labs Research Seminar, June 2011.

Coding for Flash Memories. CS Seminar, University of Massachusetts at Amherst, September 2009.

Coding for Flash Memories. IBM Research Seminar, September 2009.

Flash Memories: Properties and Algorithms. EMC seminar, August 2009.

Coding for Flash Memories. Microsoft Research Talk Series, June 2009.

Coding for Flash Memories. JPL-Caltech seminar, May 2009.

Coding for Flash Memories. ECE Colloquium, University of Minnesota, January, 2009.

Coding for Flash Memories. ECE Colloquium, Rice University, November, 2008.

Network Data Storage and Topology Control. Hitachi Global Storage Technologies Research Center, San Jose, CA, October, 2004.

High-performance File Allocation. NEC Laboratories America, Princeton, NJ, May, 2004.

Interleaving for Network Data Storage. Networking, Communications and DSP Seminar, University of California, Berkeley, November, 2003.

Dispersed Information Storage for Performance and Reliability. Information Science and Technology Seminar, California Institute of Technology, February, 2002.

Various talks at conferences (INFOCOM, ISIT, etc.) and universities.

Pioneered research on new coding theories for flash memories, including codes for rewriting data (floating codes, buffer codes, trajectory codes, etc.), rank modulation, coding-based data movement, error-scrubbing codes, etc.

The recent research on new coding theories for flash memories has interested both academia and industry. A series of new sessions in academic conferences have started in recent years, including a session entitled “Coding for Flash Memories” in the 2008 IEEE International Symposium on Information Theory (ISIT’08), a session entitled “Coding for Memories” in the 2009 Information Theory and Applications Workshop (ITA’09), a session entitled “Coding for Storage” in ISIT’09, two sessions entitled “Error-correcting Codes for Flash Memories” and “Coding for Memories” in ITA’10, two sessions entitled “Rank Modulation” and “Coding for Memories” in ISIT’10, a session entitled “Coding for Memories” in ITW’10, two sessions entitled “Coding for Storage” and “New Coding Applications” in ITA’11, four sessions entitled “Coding for Memories” and one session entitled “Storage Capacities” at ISIT’11, etc.

### Professional Service

*Guest Editor, EURASIP Journal on Advances in Signal Processing, Special Issue on Coding and Signal Processing for Non-volatile Memories, 2012.*

*Program Committee, Member, IEEE Globecom, Ad-hoc and Sensor Networking Symposium (AHSN), Anaheim, CA, USA, December 2012.*

*Program Committee, Member, IEEE International Symposium on Personal, Indoor and Mobile Radio Communications (PIMRC), Sydney, Australia, September 2012.*

*Program Committee, Member, 1st IEEE International Conference on Communications in China: Communications QoS and Reliability (ICCC’12-CQR), Beijing, China, August 2012.*

*Program Committee, Member, IEEE International Conference on Computer Communication Networks (ICCCN 2012), Munich, Germany, July-August 2012.*

*Program Committee, Member, International Conference on Selected Topics in Mobile and Wireless Networking (iCOST), Avignon, France, July 2012.*

*Program Committee, Member, IEEE International Conference on Communications - Workshop on Emerging Data Storage Technologies (ICC’2012-WS), Ottawa, Canada, June 2012.*

*Program Committee, Member, IEEE International Conference on Communications - Wireless Networks Symposium (ICC’2012-WN), Ottawa, Canada, June 2012.*

*Program Committee, Member, IEEE International Conference on Communications - Ad-hoc and Sensor Networking Symposium (ICC’2012-AHSN), Ottawa, Canada, June 2012.*

*Program Committee, Member, IEEE International Conference on Distributed Computing in Sensor Systems (DCOSS), Hangzhou, China, May 2012.*

*Program Committee, Member, 3rd UCSD Non-volatile Memories Workshop (NVMW), San Diego, CA, March 2012.*

- Organizing Committee*, Co-chair of Symposium on Data Storage for Communications, IEEE International Conference on Computing, Networking and Communications (ICNC 2012), Maui, Hawaii, January 2012.
- Program Committee*, Member, IEEE Globecom, Selected Areas in Communications Symposium (SAS), Data Storage Track, Houston, TX, USA, December 2011.
- Program Committee*, Member, IEEE Globecom, Ad-hoc and Sensor Networking Symposium (AHSN), Houston, TX, USA, December 2011.
- Program Committee*, Member, International Conference on Selected Topics in Mobile and Wireless Networking (iCOST), Shanghai, China, October 2011.
- Program Committee*, Member, IEEE International Conference on Computer Communication Networks (ICCCN 2011), Maui, Hawaii, August 2011.
- Program Committee*, Member, IEEE International Conference on Communications - Ad-hoc, Sensor and Mesh Networking Symposium (ICC'2011-AHSM), Kyoto, Japan, June 2011.
- Program Committee*, Member, IEEE International Conference on Communications - Wireless Networking Symposium (ICC'2011-WNS), Kyoto, Japan, June 2011.
- Program Committee*, Member, 2nd UCSD Non-volatile Memories Workshop (NVMW), San Diego, CA, March 2011.
- Program Committee*, Member, IEEE Wireless Communications & Networking Conference (WCNC), Cancun, Mexico, March 2011.
- Chair*, IEEE Communications Society 2010 Best Paper Award Committee on Data Storage, 2011.
- Organizing Committee*, co-organizer and co-chair, IEEE Global Communications Conference - Workshop on The Application of Communication Theory to Emerging Memory Technologies, Miami, Florida, USA, December 2010.
- Program Committee*, Member, International Conference on Mobile Ad-hoc and Sensor Networks (MSN), China, December 2010.
- Program Committee*, Member, IEEE Globecom, Miami, Florida, USA, December 2010.
- Program Committee*, Member, International Symposium on Stabilization, Safety and Security of Distributed Systems (SSS 2010), New York, USA, September 2010.
- Program Committee*, Member, IEEE International Conference on Computer Communication Networks (ICCCN 2010), Zurich, Switzerland, August 2010.
- Program Committee*, Member, IEEE International Conference on Communications - Ad-hoc, Sensor and Mesh Networking Symposium (ICC'2010-AHS), Cape Town, South Africa, May 2010.
- Program Committee*, Member, International Conference on Mobile Ad-hoc and Sensor Networks (MSN), China, December 2009.
- Program Committee*, Member, IEEE International Conference on Computer Communication and Networks (ICCCN 2009), San Francisco, CA, USA, August 2009.
- Program Committee*, Member, IEEE International Conference on Communications - Adhoc and Sensor Networking Symposium (ICC'2009-AHSNET), Dresden, Germany, June 2009.

*Program Committee*, Member, International Conference on Mobile Ad-hoc and Sensor Networks (MSN), Wuhan, China, December 2008.

*Program Committee*, Member, 7th IEEE International Symposium on Network Computing and Applications (NCA2008), Cambridge, MA, USA, July 2008.

*Program Committee*, Member, IEEE NCA2006 - Workshop on Trustworthy Network Computing, Cambridge, MA, USA, July 2006.

Panelist for NSF (National Science Foundation) and GIF (German-Israeli Foundation).

### Other Professional Activities

Gave talks at various academic conferences and workshops.

Peer reviewer for top academic journals and conferences, including ACM Transactions on Sensor Networks, ACM Transactions on Storage, Algorithmica, IEEE Transactions on Computers, IEEE Transactions on Information Theory, IEEE Transactions on Mobile Computing, IEEE Transactions on Parallel and Distributed Systems, IEEE Journal on Selected Areas in Communications, SIAM Journal on Discrete Mathematics, IEEE ISIT, IEEE INFOCOM, IEEE ICCCN, IEEE ICDCS, International Conference on Dependable Systems and Networks, International Conference on Information Processing in Sensor Networks, etc.

### Publications

#### On coding for non-volatile memories

H. Zhou, A. Jiang and J. Bruck, Systematic Error-correcting Codes for Rank Modulation, to appear in *Proc. IEEE International Symposium on Information Theory (ISIT)*, Cambridge, MA, July 1-6, 2012.

E. En Gad, A. Jiang and J. Bruck, Trade-offs between Instantaneous and Total Capacity in Multi-cell Flash Memories, to appear in *Proc. IEEE International Symposium on Information Theory (ISIT)*, Cambridge, MA, July 1-6, 2012.

M. Salajegheh, Y. Wang, A. Jiang, E. Learned-Miller and K. Fu, Half-Wits: Software Techniques for Low-Voltage Probabilistic Storage on Microcontrollers with NOR Flash Memory, to appear in *ACM Transactions on Embedded Computing Systems, Special Issue on Probabilistic Embedded Computing*, 2012.

A. Jiang, H. Li and J. Bruck, On the Capacity and Programming of Flash Memories, in *IEEE Transactions on Information Theory*, vol. 58, no. 3, pp. 1549–1564, March 2012.

M. Salajegheh, Y. Wang, A. Jiang, E. Learned-Miller and K. Fu, Half-Wits: Software Techniques for Low-voltage Probabilistic Storage on Microcontrollers with NOR Flash Memory, in *Proc. 3rd Annual Non-Volatile Memories Workshop (NVMW)*, UCSD, San Diego, CA, March 2012.

A. Jiang, H. Zhou and J. Bruck, Variable-level Cells for Nonvolatile Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 2489–2493, St. Petersburg, Russia, August 2011.

A. Jiang, H. Zhou, Z. Wang and J. Bruck, Patterned Cells for Phase Change Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 2294–2298, St. Petersburg, Russia, August 2011.

- E. En Gad, A. Jiang and J. Bruck, Compressed Encoding for Rank Modulation, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 849–853, St. Petersburg, Russia, August 2011.
- H. Zhou, A. Jiang and J. Bruck, Nonuniform Codes for Correcting Asymmetric Errors, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 1011–1015, St. Petersburg, Russia, August 2011.
- H. Zhou, A. Jiang and J. Bruck, Error-correcting Schemes with Dynamic Thresholds in Nonvolatile Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 2109–2113, St. Petersburg, Russia, August 2011.
- Y. Wu and A. Jiang, Position Modulation Code for Rewriting Write-Once Memories, in *IEEE Transactions on Information Theory*, vol. 57, no. 6, pp. 3692–3697, 2011.
- M. Salajegheh, Y. Wang, K. Fu, A. Jiang and E. Learned-Miller, Exploiting Half-Wits: Smarter Storage for Low-Power Devices, in *Proc. 9th USENIX Conference on File and Storage Technologies (FAST)*, San Jose, CA, Feb. 2011.
- A. Jiang, H. Zhou and J. Bruck, Maximizing the Storage Capacity of Nonvolatile Memories, in *Proc. Information Theory and Applications (ITA) Workshop*, UCSD, San Diego, CA, Feb. 2011.
- A. Jiang and Y. Wang, Rank Modulation with Multiplicity, in *Proc. IEEE Workshop on Application of Communication Theory to Emerging Memory Technologies (ACTEMT)*, pp. 1928–1932, Miami, FL, Dec. 2010.
- A. Jiang, V. Bohossian and J. Bruck, Rewriting Codes for Joint Information Storage in Flash Memories, in *IEEE Transactions on Information Theory*, vol. 56, no. 10, pp. 5300–5313, October 2010.
- A. Jiang, R. Mateescu, E. Yaakobi, J. Bruck, P. Siegel, A. Vardy and J. Wolf, Storage Coding for Wear Leveling in Flash Memories, in *IEEE Transactions on Information Theory*, vol. 56, no. 10, pp. 5290–5299, October 2010.
- A. Jiang, J. Bruck and H. Li, Constrained Codes for Phase-change Memories, in *Proc. IEEE Information Theory Workshop (ITW)*, Dublin, Ireland, August–September 2010.
- E. Yaakobi, A. Jiang, P. Siegel, A. Vardy and J. Wolf, On the Parallel Programming of Flash Memory Cells, in *Proc. IEEE Information Theory Workshop (ITW)*, Dublin, Ireland, August–September 2010.
- A. Jiang and J. Bruck, Data Representation for Flash Memories, book chapter in *Data Storage*, ISBN 978-953-307-063-6, In-Tech Publisher, 2010.
- A. Jiang, M. Langberg, R. Mateescu and J. Bruck, Data Movement and Aggregation in Flash Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 1918–1922, Austin, TX, June 2010.
- F. Zhang, H. Pfister and A. Jiang, LDPC Codes for Rank Modulation in Flash Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT)*, pp. 859–863, Austin, TX, June 2010.
- A. Jiang, M. Schwartz and J. Bruck, Correcting Charge-constrained Errors in the Rank Modulation Scheme, in *IEEE Transactions on Information Theory*, vol. 56, no. 5, pp. 2112–2120, May 2010.

- A. Jiang, M. Langberg, R. Mateescu and J. Bruck, Data Movement in Flash Memories, in *Proc. 47th Annual Allerton Conference on Communication, Control and Computing (Allerton'09)*, pp. 1031-1038, Monticello, IL, September 2009.
- A. Jiang and J. Bruck, Information Representation and Coding for Flash Memories, in *Proc. IEEE Pacific Rim Conference on Communications, Computers and Signal Processing (PACRIM)*, pp. 920-925, Victoria, B.C., Canada, August 2009.
- A. Jiang and H. Li, Optimized Cell Programming for Flash Memories, in *Proc. IEEE Pacific Rim Conference on Communications, Computers and Signal Processing (PACRIM)*, pp. 914-919, Victoria, B.C., Canada, August 2009.
- A. Jiang, R. Mateescu, E. Yaakobi, J. Bruck, P. Siegel, A. Vardy and J. Wolf, Storage Coding for Wear Leveling in Flash Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT'09)*, pp. 1229-1233, June-July 2009.
- A. Jiang, M. Langberg, M. Schwartz and J. Bruck, Universal Rewriting in Constrained Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT'09)*, pp. 1219-1223, June-July 2009.
- Z. Wang, A. Jiang and J. Bruck, On the Capacity of Bounded Rank Modulation for Flash Memories, in *Proc. IEEE International Symposium on Information Theory (ISIT'09)*, pp. 1234-1238, June-July 2009.
- A. Jiang, R. Mateescu, M. Schwartz and J. Bruck, Rank Modulation for Flash Memories, in *IEEE Transactions on Information Theory*, vol. 55, no. 6, pp. 2659-2673, June 2009.
- A. Jiang, H. Li and Y. Wang, Error Scrubbing Codes for Flash Memories, in *Proc. Canadian Workshop on Information Theory (CWIT)*, pp. 32-35, May 2009.
- A. Jiang and J. Bruck, On The Capacity of Flash Memories, *Proc. International Symposium on Information Theory and Its Applications (ISITA'08)*, pp. 94-99, December 2008.
- A. Jiang, R. Mateescu, M. Schwartz and J. Bruck, Rank Modulation for Flash Memories, *Proc. IEEE International Symposium on Information Theory (ISIT'08)*, pp. 1731-1735, July 2008.
- A. Jiang, M. Schwartz and J. Bruck, Error-Correcting Codes for Rank Modulation, *Proc. IEEE International Symposium on Information Theory (ISIT'08)*, pp. 1736-1740, July 2008.
- A. Jiang and J. Bruck, Joint Coding for Flash Memory Storage, *Proc. IEEE International Symposium on Information Theory (ISIT'08)*, pp. 1741-1745, July 2008.
- A. Jiang, On The Generalization of Error-Correcting WOM Codes, *Proc. IEEE International Symposium on Information Theory (ISIT'07)*, pp. 1391-1395, June 2007.
- A. Jiang, V. Bohossian and J. Bruck, Floating Codes for Joint Information Storage in Write Asymmetric Memories, *Proc. IEEE International Symposium on Information Theory (ISIT'07)*, pp. 1166-1170, June 2007.
- V. Bohossian, A. Jiang and J. Bruck, Buffer Coding for Asymmetric Multi-Level Memory, *Proc. IEEE International Symposium on Information Theory (ISIT'07)*, pp. 1186-1190, June 2007.

#### **On network data storage and data interleaving**

- A. Jiang, M. Cook and J. Bruck, Optimal Interleaving on Tori, *SIAM Journal on Discrete Mathematics*, vol. 20, no. 4, pp. 841-879, 2006.

- A. Jiang, Network Coding for Joint Storage and Transmission with Minimum Cost, *Proc. IEEE International Symposium on Information Theory (ISIT'06)*, pp. 1359-1363, July 2006.
- A. Jiang and J. Bruck, Network File Storage with Graceful Performance Degradation, *ACM Transactions on Storage*, vol. 1, no. 2, pp. 171-189, May 2005.
- A. Jiang and J. Bruck, Multicluster Interleaving on Paths and Cycles, *IEEE Transactions on Information Theory*, vol. 51, no. 2, pp. 597-611, February 2005.
- A. Jiang and J. Bruck, Coding over Graphs, book chapter in *Information, Coding and Mathematics*, pp. 355-364, Kluwer Academic Publishers, 2002.
- A. Jiang, M. Cook and J. Bruck, Optimal t-Interleaving on Tori, *Proc. IEEE International Symposium on Information Theory (ISIT'04)*, pp. 22, June–July, 2004.
- A. Jiang and J. Bruck, Multi-Cluster Interleaving on Linear Arrays and Rings, *Proc. International Symposium on Communication Theory and Applications*, pp. 112-117, July 2003.
- A. Jiang and J. Bruck, Memory Allocation in Information Storage Networks, *Proc. IEEE International Symposium on Information Theory (ISIT'03)*, pp. 453, June–July, 2003.
- A. Jiang and J. Bruck, Optimal Content Placement for En-route Web Caching, *Proc. the 2nd IEEE International Symposium on Network Computing and Applications*, pp. 9-16, April 2003.
- A. Jiang and J. Bruck, Diversity Coloring for Information Storage in Networks, *Proc. IEEE International Symposium on Information Theory (ISIT'02)*, pp. 381, June–July, 2002.

#### **On wireless ad hoc communication and sensor networks**

- F. Zhang, A. Jiang and J. Chen, On the Planarization of Wireless Sensor Networks, in *Algorithmica*, vol. 60, no. 3, pp. 593–608, March 2011.
- J. Chen, A. Jiang, I. A. Kanj, G. Xia and F. Zhang, Separability and Topology Control of Quasi Unit Disk Graphs, in Springer *Wireless Networks (WINET) Journal*, vol. 17, no. 1, pp. 53–67, January 2011.
- J. Bruck, J. Gao and A. Jiang, Localization and Routing in Sensor Networks by Local Angle Information, *ACM Transactions on Sensor Networks*, vol. 5, no. 1, article no. 7, February 2009.
- F. Zhang, A. Jiang and J. Chen, Sorting Based Data Centric Storage, *Proc. the 7th IEEE International Symposium on Network Computing and Applications (NCA)*, pp. 283-286, July 2008.
- H. Lu, A. Jiang and S. Liu, Locality Sensitive Information Brokerage in Distributed Sensor Networks, *Proc. the 28th IEEE International Conference on Distributed Computing Systems (ICDCS)*, pp. 522-529, June 2008.
- F. Zhang, A. Jiang and J. Chen, Robust Planarization of Unlocalized Wireless Sensor Networks, *Proc. the 27th IEEE INFOCOM*, pp. 798-806, April 2008.
- J. Bruck, J. Gao and A. Jiang, MAP: Medial Axis Based Geometric Routing in Sensor Networks, in Springer *WINET (Wireless Networks) Journal*, vol. 13, no. 6, pp. 835-853, December 2007.
- H. Lu, S. Liu and A. Jiang, A Cross-layer Design for End-to-end On-demand Bandwidth Allocation in Infrastructure Wireless Mesh Networks, *Proc. WASA*, pp. 55-62, August 2007.

- F. Zhang, H. Li, A. Jiang, J. Chen and P. Luo, Face Tracing Based Geographic Routing in Non-planar Wireless Networks, *Proc. the 26th IEEE INFOCOM*, pp. 2243-2251, May 2007.
- J. Chen, A. Jiang, I. A. Kanj, G. Xia and F. Zhang, Separability and Topology Control of Quasi Unit Disk Graphs, *Proc. the 26th IEEE INFOCOM*, pp. 2225-2233, May 2007.
- J. Bruck, J. Gao and A. Jiang, MAP: Medial Axis Based Geometric Routing in Sensor Networks, *Proc. the 11th ACM International Conference on Mobile Computing and Networking (MobiCom'05)*, pp. 88-102, August–September, 2005.
- J. Bruck, J. Gao and A. Jiang, Localization and Routing in Sensor Networks by Local Angle Information, *Proc. the 6th ACM International Symposium on Mobile Ad Hoc Networking and Computing (MobiHoc'05)*, pp. 181-192, May 2005.
- A. Jiang and J. Bruck, Monotone Percolation and The Topology Control of Wireless Networks, *Proc. the 24th IEEE INFOCOM*, vol. 1, pp. 327-338, March 2005.

#### **On data structures for membership query**

- J. Bruck, J. Gao and A. Jiang, Weighted Bloom Filter, *Proc. IEEE International Symposium on Information Theory (ISIT'06)*, pp. 2304-2308, July 2006.

#### **On remote surveillance and network data communication**

- Z. Wang, A. Jiang and Z. Kang, A Remote-Surveillance System Using Digital Image Processing Technology, *Computers and Networks*, vol. 10, pp. 17-18, May 1999.

**Link to Publications:** <http://faculty.cs.tamu.edu/ajiang>