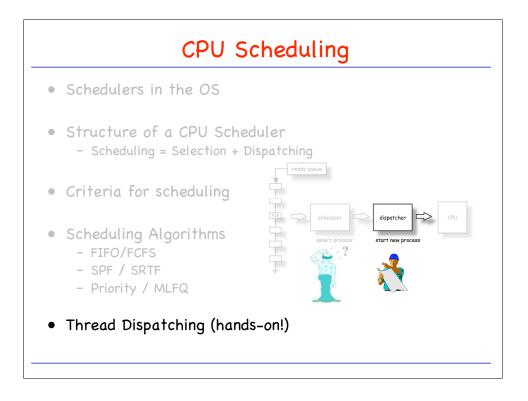
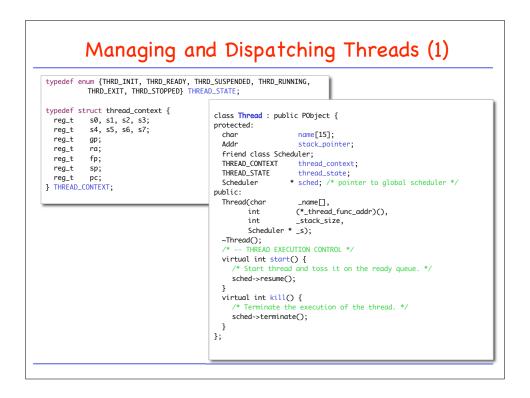
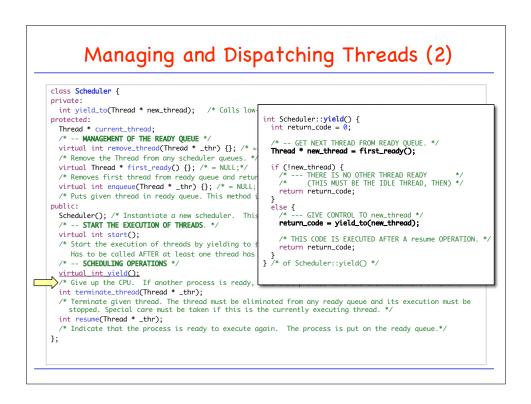


CPSC 410 / 611 : Operating Systems

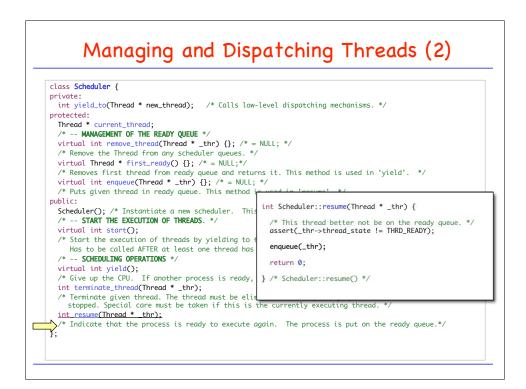


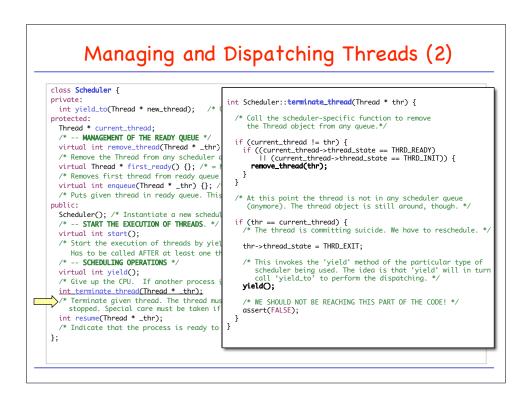


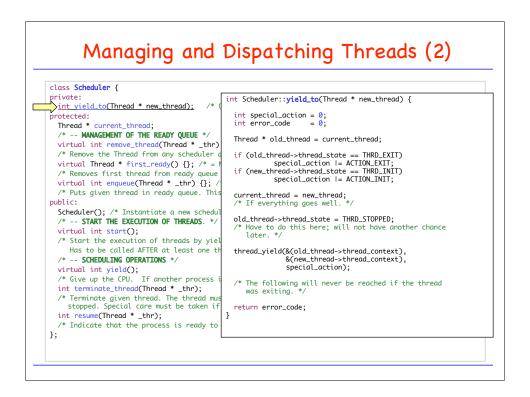


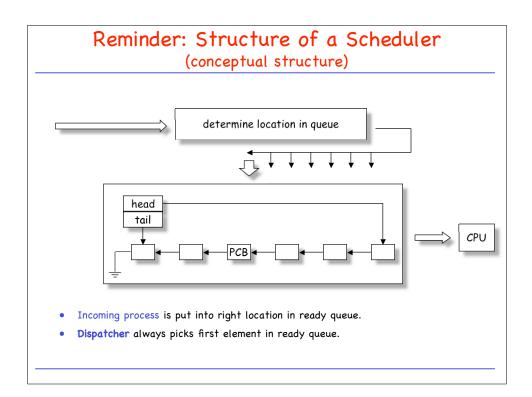


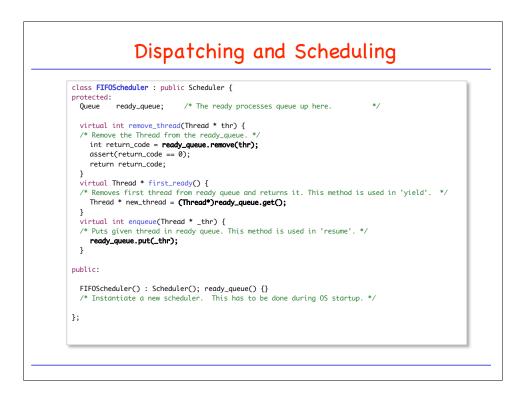
CPSC 410 / 611 : Operating Systems











	Low-Level Dispatching, MIPS-style	
LEA	thread_yield)	
	# a0 : pointer to current thread's context frame	
	# a1 : pointer to new thread's context frame	
	≇ a2 .AND. ACTION_INIT != 0 -> new thread just initialized.	
	$\#$ a2 .AND. ACTION_EXIT != 0 -> old thread exits. do not save state.	
	<pre># : other -> simple context switch.</pre>	
	ti t1, ACTION_EXIT	
	and t3, t1, a2	
	onez t3, start_switch # IF THREAD EXISTS, SKIP STATE SAVING	
	# IF THREAD IS EXITING, POINTER TO PROCESSOR STATE TABLE IS LIKELY INVALID.	
	sw s0, S0_OFF(d0) # SAVE CURRENT STATE	
	sw s6, S6_OFF(a0)	
	sw s7, S7_OFF(a0)	
	sw gp, GP_OFF(a0)	
	sw ra, RA_OFF(a0)	
	sw fp, FP_OFF(a0)	
	sw sp, SP_OFF(a0)	
sta	_switch:	
	Lw s0, S0_OFF(a1) # LOAD REGISTERS FOR NEW TASK	
	lw s7, S7_0FF(a1)	
#	lw gp, GP_OFF(a1)	
	lw ra, RA_OFF(a1)	
	lw fp, FP_OFF(a1)	
	lw sp, SP_OFF(a1)	
	(continue on next slide)	

